

HPC BENCHMARK GAME: COMPARING PROGRAMMING LANGUAGES REGARDING ENERGY-EFFICIENCY

for Applications from the HPC Field



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MOTIVATION

- Energy efficiency is important (for HPC ...) → see Green500 list
“The Green500 is a biannual ranking of supercomputers, from the TOP500 list of supercomputers, in terms of energy efficiency. The list measures performance per watt using the TOP500 measure of high performance LINPACK benchmarks at double-precision floating-point format.” [from Wikipedia]
- Inspired by Pereira et al.’s research on runtime vs. energy efficiency in the Computer Language Benchmarks Game (CLBG) and the Rosetta Code.
- Most of those are not representative for HPC applications
- HPC Benchmark Game aims to address the gap with HPC-relevant languages and benchmarks

HPC BENCHMARK GAME

Languages chosen: C, C++, Fortran, Julia

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Research Questions:

1. Comparison between JIT-compiled and native compiled code
2. Impact of the compiler on energy efficiency
3. Impact of hardware architecture and over-threading on energy efficiency

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Points we did not consider:

- Memory vs. Energy consumption (Pereira et al.: 89% from CPU)
- Frequency scaling policy – just used default installed governor
- Vector instruction widths (AVX2 vs. AVX512)
- Platform-specific OpenMP thread mappings
- Influence of problem size

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Criteria

Benchmarks selection criteria:

- non-trivial mathematical algorithm
- shared-memory parallelization
- and less than 2000 LOC.

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Benchmarks

One Reference benchmark chosen from each language:

- C: Cellular Automaton (CA) (based on Conways Game of Life).
9-point stencil, 8-bit integer operations.
- C++: Transport on uniform grids (TUG).
Solves diffusion with an alternating direction implicit method. (GFZ and Uni Potsdam)
- Fortran: NAS Parallel Benchmarks FT
(NPB – Fourier Transform)
- Julia: SRAD (Rodinia benchmark)
SRAD is a diffusion method for ultrasonic and radar imaging applications based on partial differential equations and is used to remove locally correlated noise.

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Re-Implementation Details

- Idiomatic re-implementation of the benchmarks
- Standardized pseudo-random number generation, for reproducibility of results across languages.
- OpenMP parallelization; Julia FLoops
- C++ used Eigen library
- Bounds checking disabled (C++ Eigen and Julia)
- Plain C-style arrays for C
- Correctness was verified (reproducibility across languages, independent of parallelization)

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Time and energy measurements by: EMA

RAPL (RUNNING AVERAGE POWER LIMIT)

- Control and real-time measurements of CPU and RAM energy consumption
- From Intel, since Sandy Bridge architecture, 2011
- Our AMD CPUs provide data for “core” and for “package” domains, but not for dram memory nor I/O
- Measured every microsecond, in microJoule
- On Linux accessible via sysfs virtual file system
/sys/class/powercap/intel-rapl*/energy_uj
- Here, we add energy values for both “package” domains.

TESTBED

Measurements on HPC2024 cluster at PIK:

- Two AMD EPYC 9554 “Genoa” CPUs (2×64 cores)
- 6 GB RAM per core $\rightarrow$ 768 GB DDR5 RAM total
- 8 cores share one L3 cache, 16 cores share 3 memory channels
- “zen4” core architecture: $2 \times$ AVX2 or $1 \times$ AVX512 vector engines

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- Intel Classic oneAPI 2023.2.0 (icc, ifort; avx2-optimization)
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Julia version 1.11.0 – package and app (`-O3 -C znver4 --check-bounds=no`)

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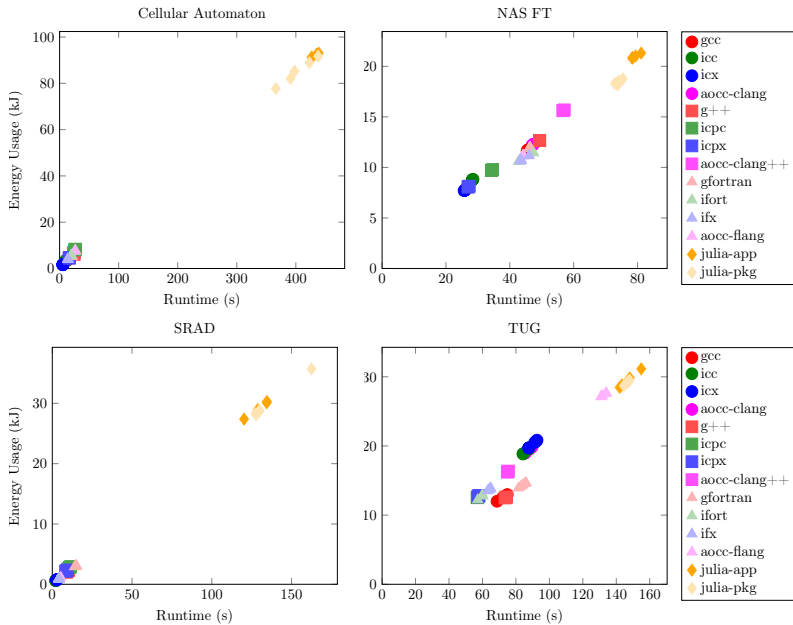
$\Rightarrow$ Use all 128 cores of one node for the measurements.

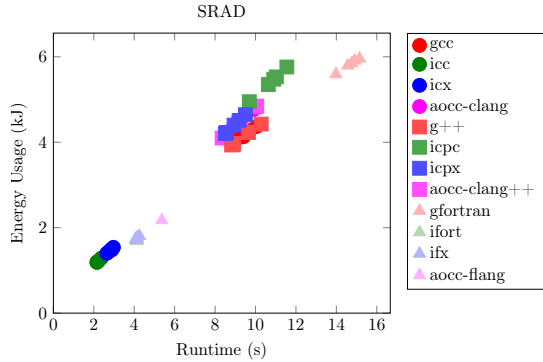
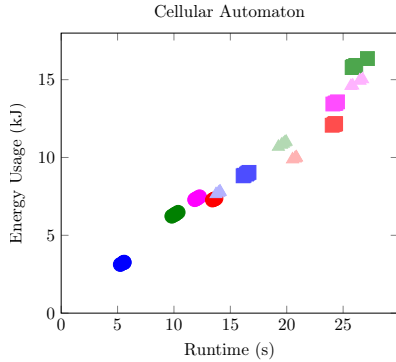
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$\Rightarrow$ All measurements repeated 5 times.

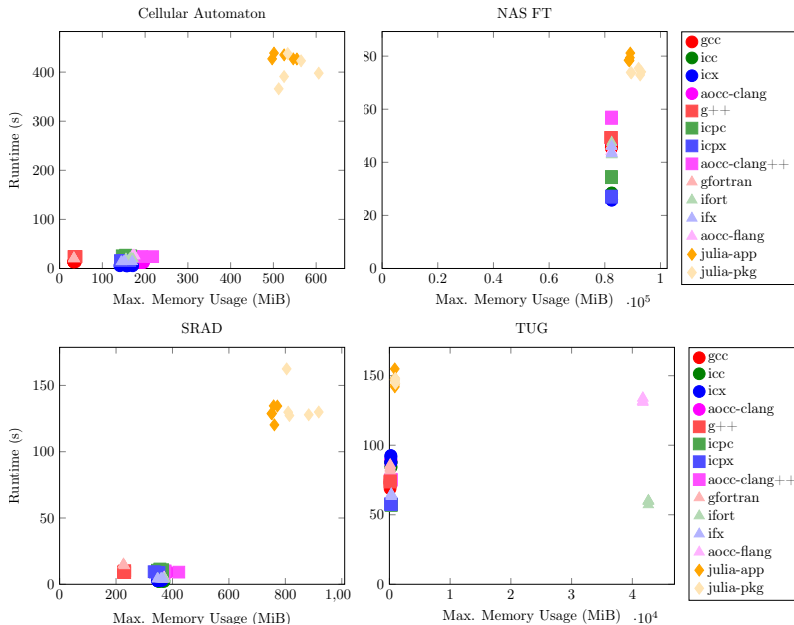


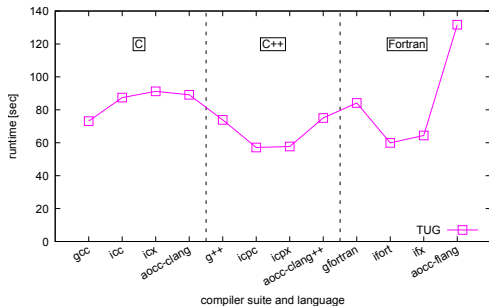
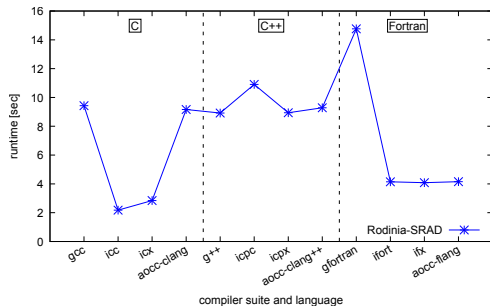
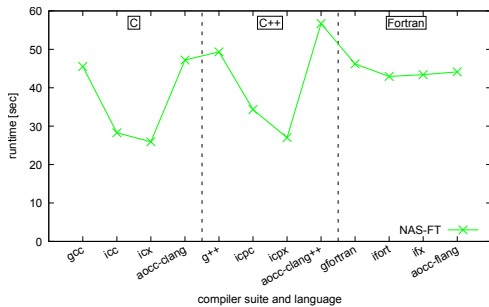
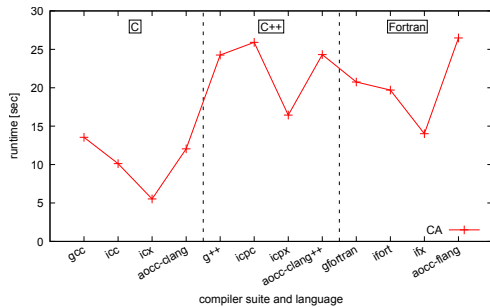


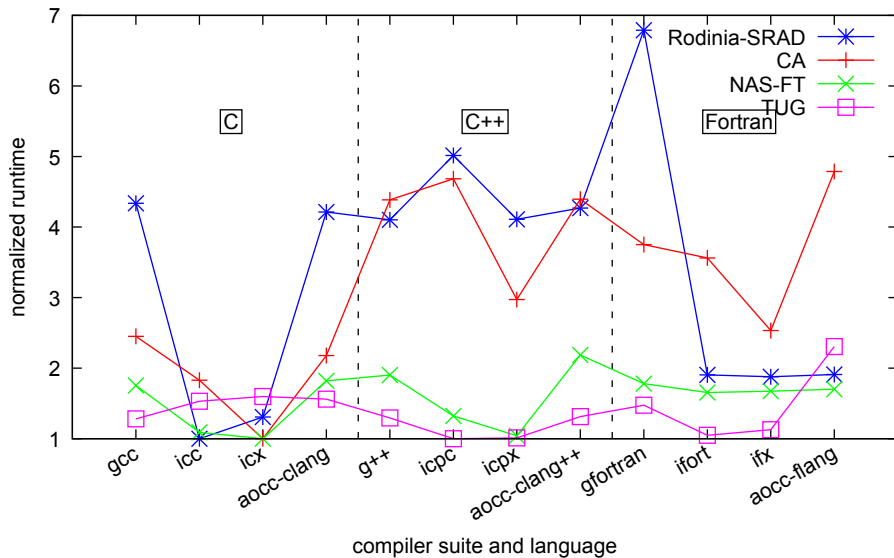
RESEARCH QUESTION 1

Comparison between JIT-compiled and native compiled code

- Energy consumption is roughly linear with runtime
- Julia is not competitive with C/C++/Fortran
- Julia app approach did not improve performance over the packaged approach







Benchmark	“Winner”
CA	C/icx
NAS FT	C/icx, C/icc, C++/icpx
SRAD	C/icc, C/icx
TUG	C++/icpc, C++/icpx, Fortran/ifort, Fortran/ifx

RESEARCH QUESTION 2

Impact of the compiler on energy efficiency

- Compiler impact is application-dependent
- Larger runtime variance for SRAD and CA
- Compiler choice can have a bigger impact than language choice
- AVX2 optimization in Intel compilers outperforms “zen4” in GNU/AMD compilers ^{⟨1⟩}

^{⟨1⟩}Treat with caution

RESEARCH QUESTION 3

Impact of hardware architecture and over-threading on energy efficiency

Scaling results for TUG:

# Threads	1	2	4	8	16	32	64	128
Runtime [s]	174	122	87	70	62	59.1	58.9	59.6
Energy [kJ]	56	40	29	23	21	21	22	25

GLOBAL RESULTS

Normalized Runtime		Normalized Energy	
C/ixc	1.20	C/ixc	1.20
C/icc	1.32	C/icc	1.37
Fortran/ixf	1.73	Fortran/ixf	1.51
Fortran/ifort	1.85	Fortran/ifort	1.60
C++/icpx	1.89	C++/icpx	1.81
C++/g++	2.11	C/gcc	1.85
C/gcc	2.21	C++/g++	2.11
C/aocc-clang	2.26	C/aocc-clang	2.14
C++/aocc-clang++	2.26	C++/aocc-clang++	2.14
C++/icpc	2.36	Fortran/aocc-flang	2.15
Fortran/aocc-flang	2.45	Fortran/gfortran	2.26
Fortran/gfortran	2.86	C++/icpc	2.30
Julia/pkg	13.32	Julia/pkg	10.66
Julia/app	13.74	Julia/app	11.23

(geometric mean over normalized results)

CONCLUSION

- Confirm correlation between runtime and energy consumption
 - Julia is not competitive with native-compiled languages
 - Negative effect of over-threading on energy-efficiency is shown
 - Compiler has higher impact than chosen programming language
- Make users aware to try different compilers

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Thank you!

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<https://benchmarksgame-team.pages.debian.net/benchmarksgame/index.html>

Rosetta Code <http://rosettacode.org/>

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CA <https://www.cs.uni-potsdam.de/bs/research/labsCa.html>

TUG <https://git.gfz-potsdam.de/naaice/tug>

NBP NAS Parallel Benchmarks. <https://www.nas.nasa.gov/software/npb.html>

Rodinia Rodinia Benchmark Suite. Fork at <https://github.com/JuliaParallel/rodinia>
(the original site at virginia.edu is gone)

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